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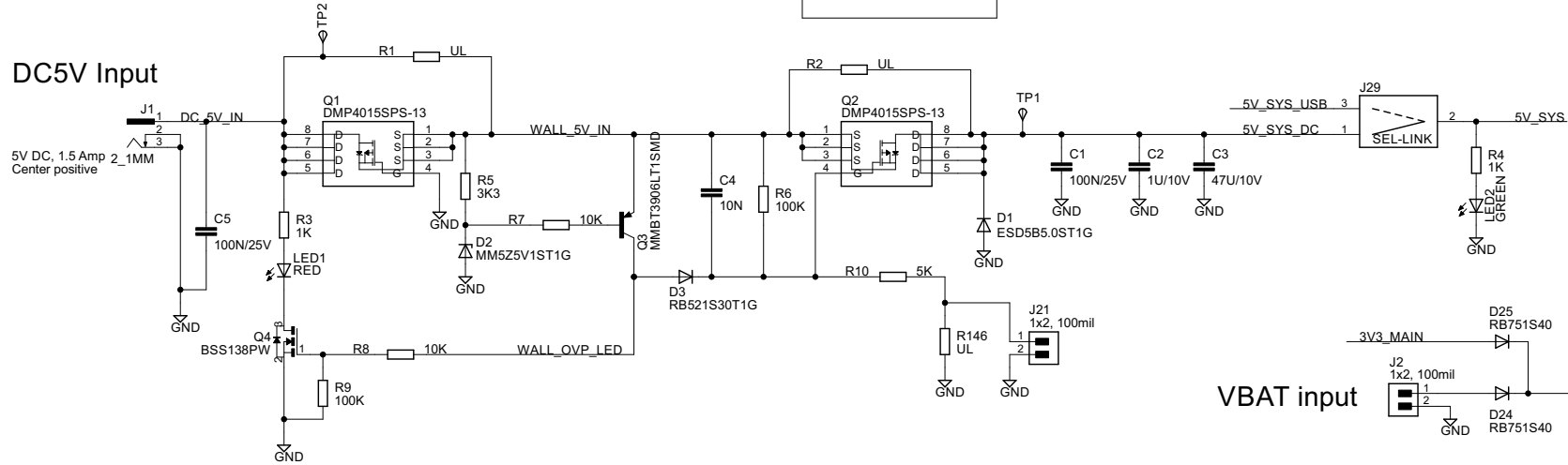
UL = UnLoaded = normally not mounted component.

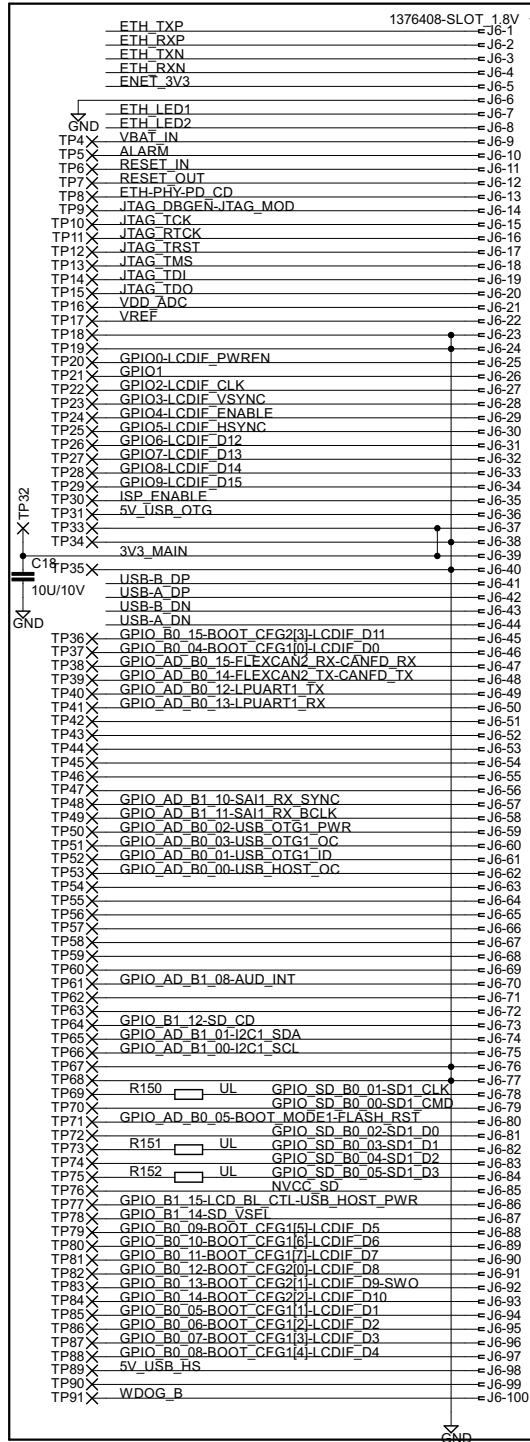
Default jumper settings are indicated in the schematic.
However, always check jumper positions on actual boards
since there is no guarantee that all jumpers are in default place.

Rev B1 - updated Corrected how RJ45 (J20) is connected.	
Rev B1 Added interface to M.2 WiFi/BT boards.	
Rev A First release.	
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Powering

DC5V Input





OEM board connector, pin 1-100
(200 pos SODIMM, 1V8 key)



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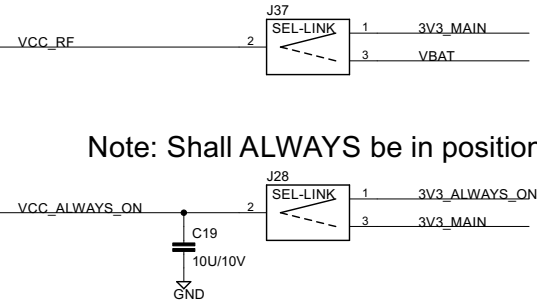
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TP92X
TP93X
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TP190X
TP191X

iMX RT1052	LPC2478/1788	LPC3250	LPC4088	LPC4357
GND	GND	GND	GND	GND
GND	GND	GND	GND	GND
GPIO_AD_B1_14-SAH1_TX_BCLK	GPIO_AD_B1_14NC	I2S1TX_CLK	NC	P3_0
GPIO_AD_B1_13-SAH1_TXD	GPIO_AD_B1_13NC	I2S1TX_SDA	NC	PC_12
GPIO_AD_B1_15-SAH1_TX_SYNC	GPIO_AD_B1_15NC	I2S1TX_WS	NC	PC_13
GPIO_SD_B1_04-FLEXSPI_CLK_B	NC	P0_0	NC	P6_0
GPIO_AD_B1_12-SAH1_RXD	GPIO_AD_B1_12NC/P5.3	GPI_00	P5.4	P6_1
EXT_PWR_EN	NC/P5.2	I2C2_SDA	P5.2	WAKEUP3
QNOFF	NC	I2C2_SCL	NC	WAKEUP2
OTG1_CHD	NC/P1.16	GPI_04	P1.16	WAKEUP1
WAKEUP	NC	GPI_06	NC	WAKEUP0
NC/BCS1	USB_CONN_IDCS1	USB_CONN_IDCS1	USB0_ID	NC
NC/P4.30	POWER_ON	POWER_ON	P4.30	SAMPLE
GPIO_AD_B1_09NC/P1.16	TST_CLK2	TST_CLK2	P1.16	CLK2_OUT
PMIC_ON_REQ	P2.14	P2.14	P2.14	P9_2
EXT_PWR_EN	P2.15	GPI_00	P2.15	P9_1
PERI_PWREN	P2.19	GPI_01	P2.19	P8_2
GPIO_B1_13	P2.21	GPI_07	P2.21	PC_2
GPIO_AD_B1_02P2.22	P2.0	P2.22	PA_1	NC
GPIO_AD_B1_03P2.23	P2.1	P2.23	PA_2	NC
GPIO_AD_B1_04P2.25	P2.2	P2.25	PA_3	NC
GPIO_AD_B1_05P2.26	P2.3	P2.26	P9_0	NC
GPIO_AD_B1_06P2.27	P2.4	P2.27	P9_1	NC
GPIO_AD_B1_07P2.30	P2.30	P2.30	PF_8	NC
CCM_CLK1_N	P2.31	P2.31	PF_9	NC
P4.28	GPO_07	P4.28	P4_3	NC
CCM_CLK1_P	P4.29	GPO_21	P4.29	P4_2
GND	GND	GND	GND	GND
GND	GND	GND	GND	GND
-SEM_C DQS	BA15	BA15	BA15	BA15
-SEM_C DM1	BQDM1/BCS2	BCS3	BCS2	BCS2
-SEM_C CLK	BA14	BA14	BA14	BA14
-SEM_C DM0	BQDM0/BCS0	BCS2	BCS0	BCS0
-SEM_C CKE	BA13	BA13	BA13	BA13
-SEM_C CAS	BCAS/BBLS3	BCS1	BBLS3	BBLS3
-SEM_C A12	BA12	BA12	BA12	BA12
-SEM_C RAS	BRAS/BBLS2	BCS0	BBLS2	BBLS2
-SEM_C A11	BA11	BA11	BA11	BA11
-SEM_C BA1	BBLS1	BBLS1	BBLS1	BBLS1
-SEM_C A10	BA10	BA10	BA10	BA10
-SEM_C BA0	BBLS0	BBLS0	BBLS0	BBLS0
-SEM_C A9	BA9	BA9	BA9	BA9
-SEM_C WE	BWE	BWE	BWE	BWE
-SEM_C A8	BA8	BA8	BA8	BA8
-SEM_C CS0	BOE	BOE	BOE	BOE
-SEM_C A7	BA7	BA7	BA7	BA7
GPIO_EMC_41	BA23	BA23	BA23	BA23
-SEM_C A6	BA6	BA6	BA6	BA6
GPIO_EMC_40	BA22	BA22	BA22	BA22
-SEM_C A5	BA5	BA5	BA5	BA5
GPIO_B1_04	BA21	BA21	BA21	BA21
-SEM_C A4	BA4	BA4	BA4	BA4
GPIO_B1_05	BA20	BA20	BA20	BA20
-SEM_C A3	BA3	BA3	BA3	BA3
GPIO_B1_06	BA19	BA19	BA19	BA19
-SEM_C A2	BA2	BA2	BA2	BA2
GPIO_B1_07	BA18	BA18	BA18	BA18
-SEM_C A1	BA1	BA1	BA1	BA1
GPIO_B1_08	BA17	BA17	BA17	BA17
-SEM_C A0	BA0	BA0	BA0	BA0
GPIO_B1_09	BA16	BA16	BA16	BA16
GPIO_B1_11	NC	BCS3	BCS3	BCS3
GPIO_B1_10	ABUF_EN/NC	ABUF_EN	GND	(PD_1)
VCC_ALWAYS_ON	VCC	VCC_EXT	VCC	VCC
GND	GND	GND	GND	GND
-SEM_C D15	BD15	BD15	BD15	BD15
-SEM_C D14	BD31/P3.31	GPI_08	BD31/P3.31	BD31
W_GPIO_25	BD14	GPO_23	BD14	BD14
-SEM_C D13	BD30/P3.30	BD13	BD30/P3.30	BD30
W_GPIO_32	BD29/P3.29	GPI_09	BD29/P3.29	BD29
-SEM_C D12	BD12	BD12	BD12	BD12
W_GPIO_36	BD28/P3.28	GPI_19	BD28/P3.28	BD28
-SEM_C D11	BD11	BD11	BD11	BD11
W_SW2	BD27/P3.27	P2.8	BD27/P3.27	BD27
-SEM_C D10	BD10	BD10	BD10	BD10
W_BOOT	BD26/P3.26	P2.9	BD26/P3.26	BD26
-SEM_C D9	BD9	BD9	BD9	BD9
W_UART_DTR	BD25/P3.25	P2.10	BD25/P3.25	BD25
-SEM_C D8	BD8	BD8	BD8	BD8
W_UART_DSR	BD24/P3.24	P2.11	BD24/P3.24	BD24
-SEM_C D7	BD7	BD7	BD7	BD7
W_UART_RTS	BD23/P3.23	P2.12	BD23/P3.23	BD23
-SEM_C D6	BD6	BD6	BD6	BD6
W_UART_CTS	BD22/P3.22	GPI_28	BD22/P3.22	BD22
-SEM_C D5	BD5	BD5	BD5	BD5
W_UART_TXD	BD21/P3.21	U2_TX	BD21/P3.21	BD21
-SEM_C D4	BD4	BD4	BD4	BD4
W_UART_RXD	BD20/P3.20	U2_RX	BD20/P3.20	BD20
-SEM_C D3	BD3	BD3	BD3	BD3
WLED_RED	BD19/P3.19	GPI_05	BD19/P3.19	BD19
-SEM_C D2	BD2	BD2	BD2	BD2
WLED_GREEN	BD18/P3.18	U2_CTS	BD18/P3.18	BD18
-SEM_C D1	BD1	BD1	BD1	BD1
WLED_BLUE	BD17/P3.17	U3_RX	BD17/P3.17	BD17
-SEM_C D0	BD0	BD0	BD0	BD0
W_OSC32K768	BD16/P3.16	U3_TX	BD16/P3.16	BD16
VCC_RF	VCC	VCC_EXT	VCC	VCC
GND	GND	GND	GND	GND

OEM board connector, pin 101-200
(200 pos SODIMM, 1V8 key)



Note: Shall ALWAYS be in position 1-2 for iMX RT OEM boards!



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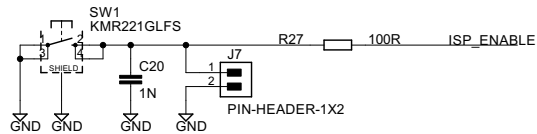
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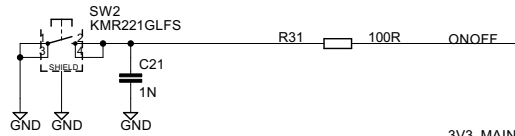
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Push-buttons and LEDs

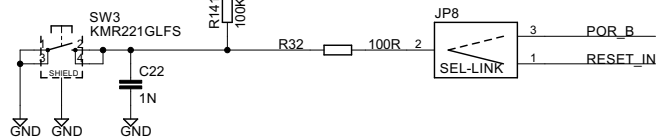
ISP Enable Key and jumper



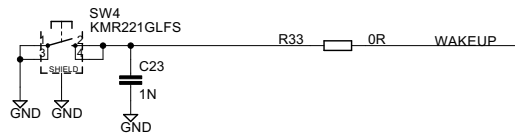
ON/OFF Key



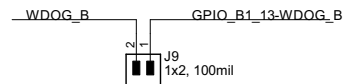
Reset Key



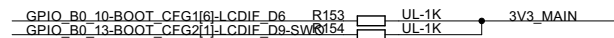
Detect Switch



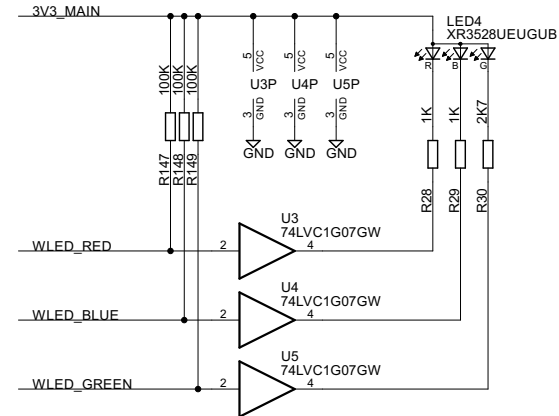
Watchdog control



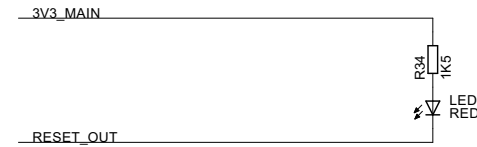
Optional i.MX RT1052/1062 boot control



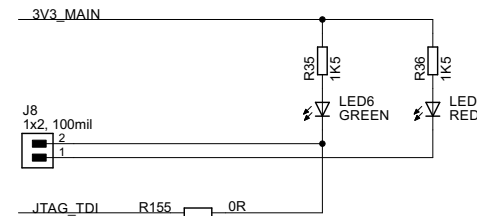
RGB-LED for RF-module



Reset LED



User LEDs



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JTAG Debug Interface

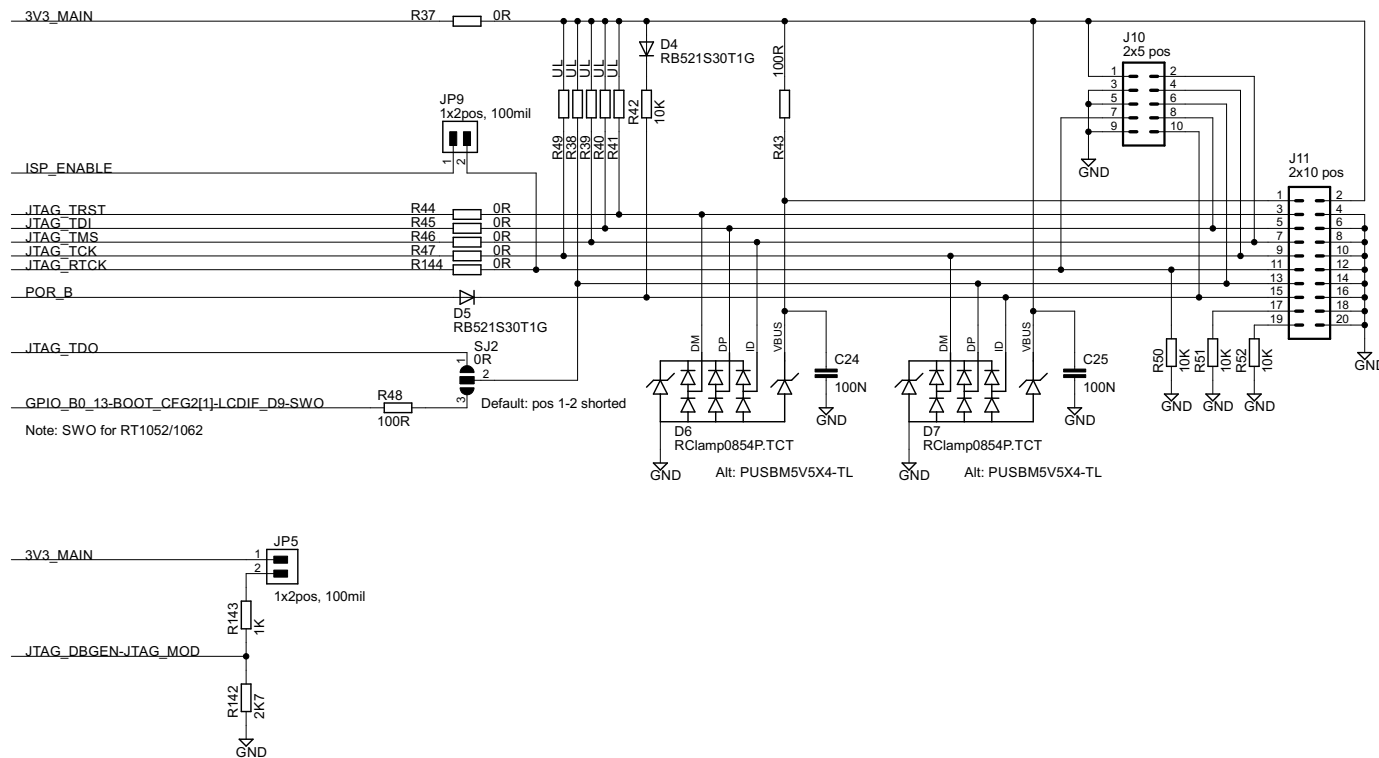
ARM 10-pin interface Serial Wire Mode

1-VCC	2-SWDIO
3-GND	4-SWCLK
5-GND	6-SWO
7-N/U	8-N/U
9-GND	10-RESET

ARM 10-pin interface JTAG Mode

1-VCC	2-TMS
3-GND	4-TCLK
5-GND	6-TDO
7-RTCK	8-TDI
9-GND	10-RESET

10 pos (50 mil pitch) connector



20 pos (100 mil pitch) connector

ARM 20-pin interface Serial Wire Mode

1-VCC (Vtref)	2-Optional VCC (Vtref)
3-N/U	4-GND
5-N/U	6-GND
7-SWDIO	8-GND
9-SWCLK	10-GND
11-N/U	12-GND
13-SWO	14-GND
15-RESET	16-GND
17-N/C	18-GND
19-N/C	20-GND

ARM 20-pin interface JTAG Mode

1-VCC (Vtref)	2-Optional VCC (Vtref)
3-N/C (TRST)	4-GND
5-TDI	6-GND
7-TMS	8-GND
9-TCLK	10-GND
11-RTCK	12-GND
13-TDO	14-GND
15-RESET	16-GND
17-N/C	18-GND
19-N/C	20-GND



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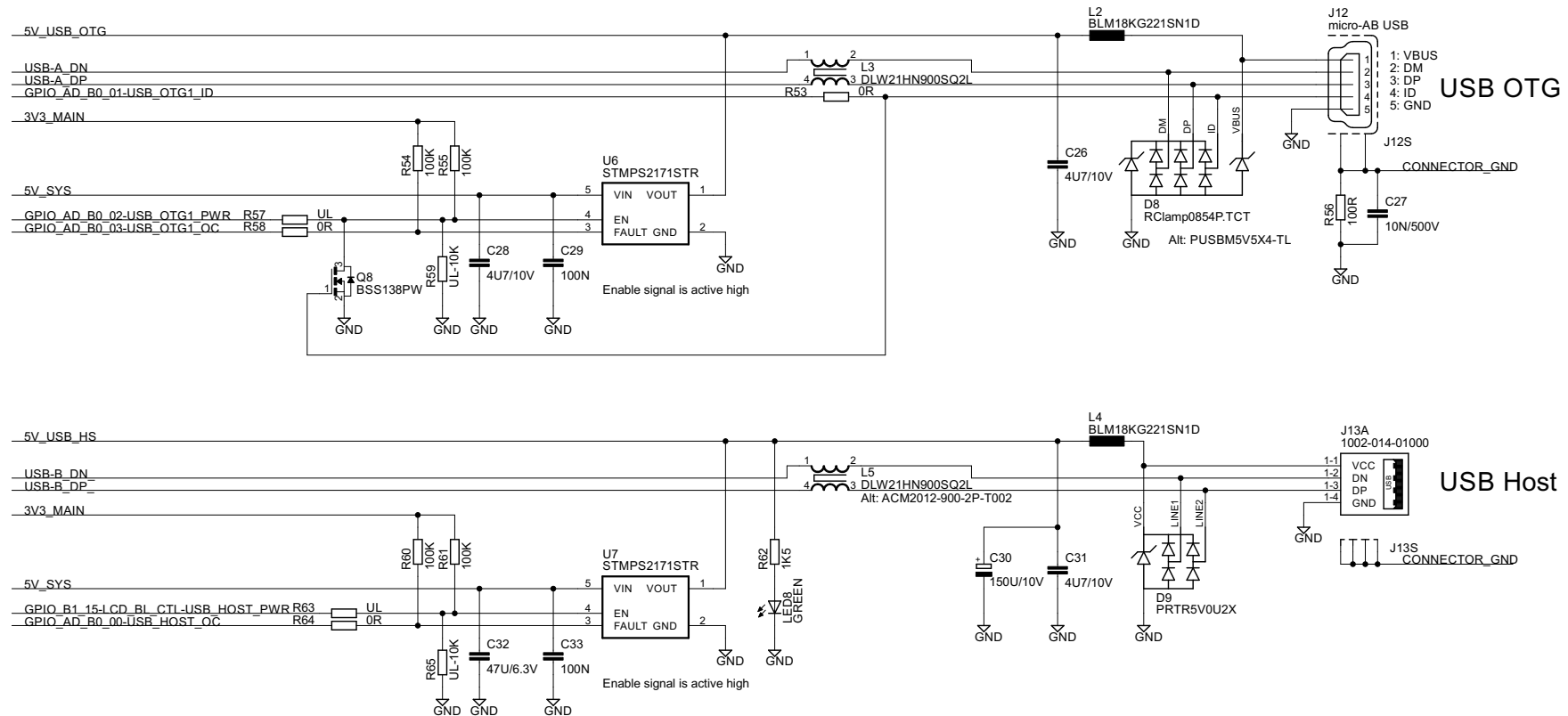
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USB Interfaces

OEM Board	USB-A ^{OTG or Device}	USB-B ^{Host}
iMX RT1052/1062	OTG1	OTG2
LPC1788	USB-2	USB-1
LPC2478	USB-2	USB-1
LPC3250	USB	Not connected
LPC4088	USB-2	USB-1
LPC4357	USB0	USB1



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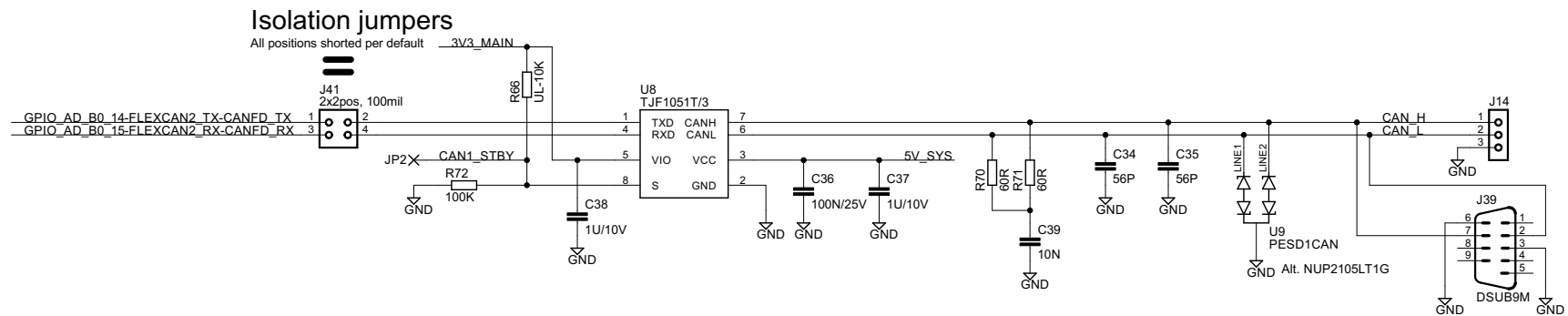
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CAN Transceiver



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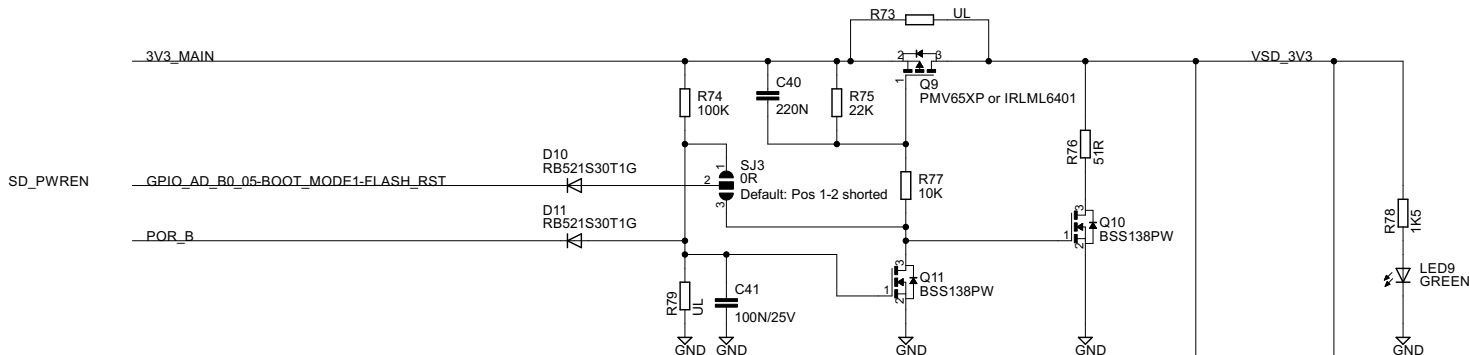
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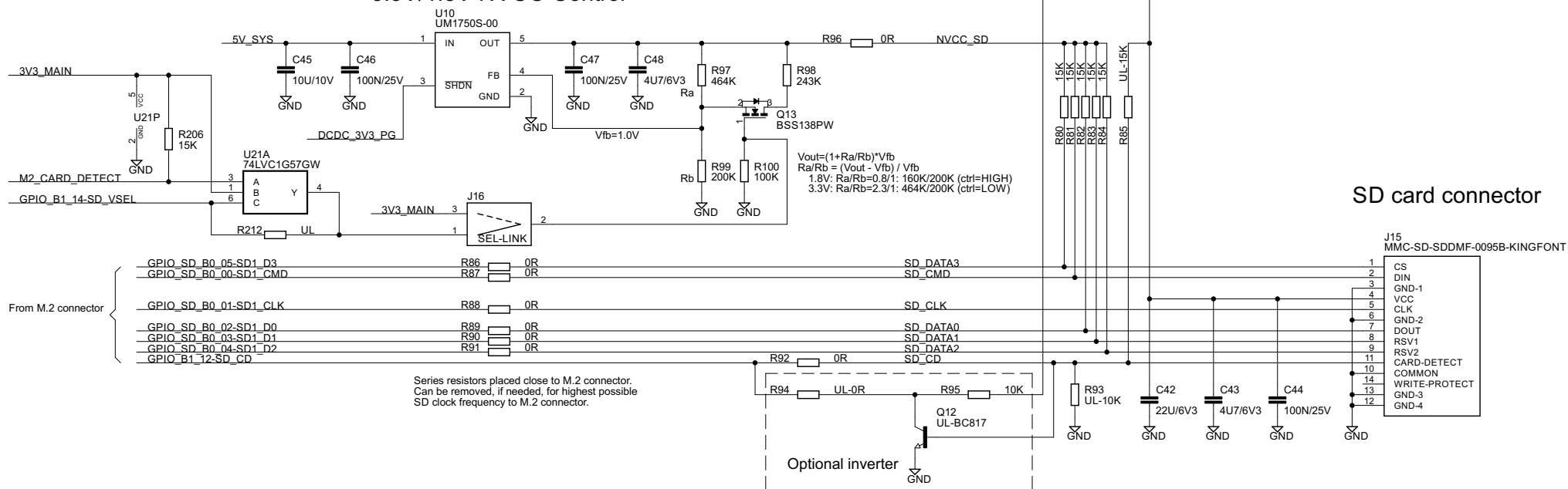
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SD Card Interface

Power Switch for SD Card Interface



3.3V/1.8V NVCC Control



M2_CARD_DETECT 0=M.2 card inserted 1=M.2 card not inserted	GPIO_B1_14-SD_VSEL 0=Set NVCC_SD to 3.3V 1=Set NVCC_SD to 1.8V	NVCC_SD control 0=NVCC_SD is 3.3V 1=NVCC_SD is 1.8V
0	0	1 Force NVCC_SD to 1.8V when M.2 card inserted
0	1	1 Force NVCC_SD to 1.8V when M.2 card inserted
1	0	0 Follow GPIO_B1_14-SD_VSEL when when M.2 card not inserted
1	1	1 Follow GPIO_B1_14-SD_VSEL when when M.2 card not inserted



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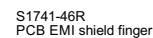
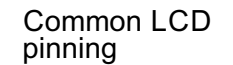
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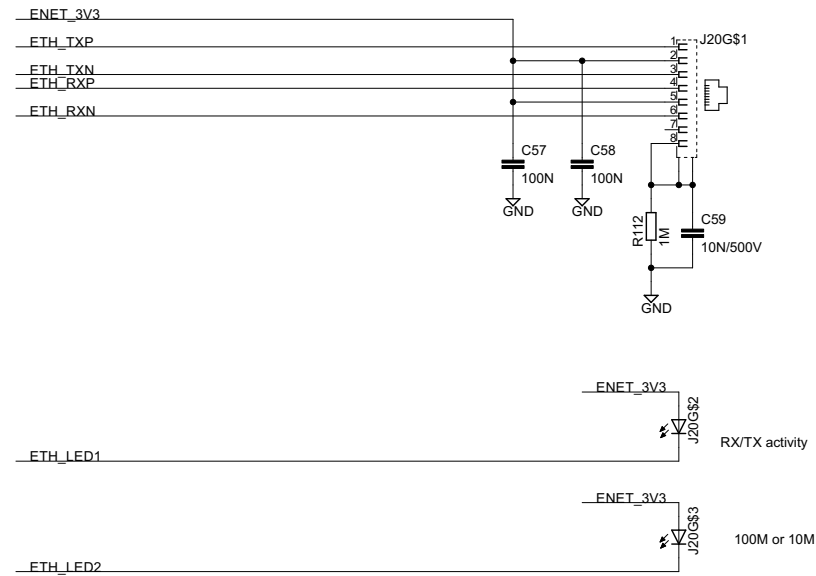
LCD Interface



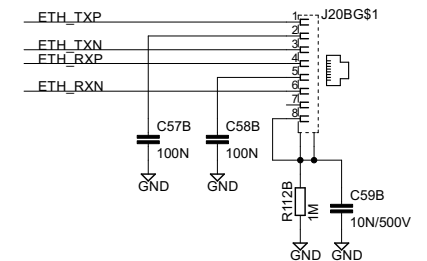
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Ethernet Interface

RJ45 Connector with integrated magnetics



Corrected schematic



The schematic to the left is the one on the board. On a custom carrier board the schematic should be corrected according to this frame (the transformers center taps should just be connected to 100nF capacitors - no supply voltage).



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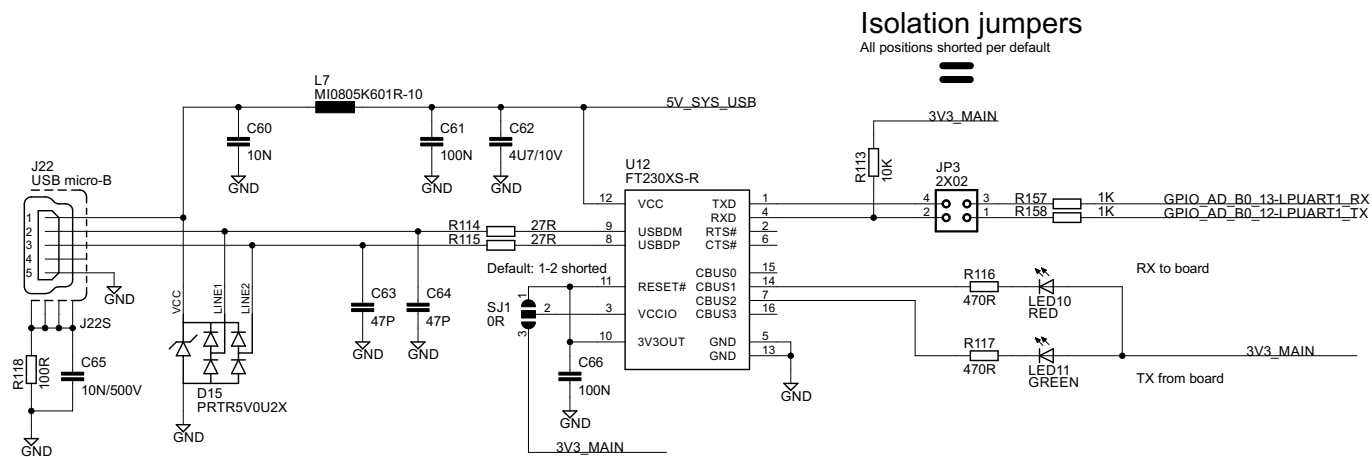
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UART-to-USB Bridge Interface



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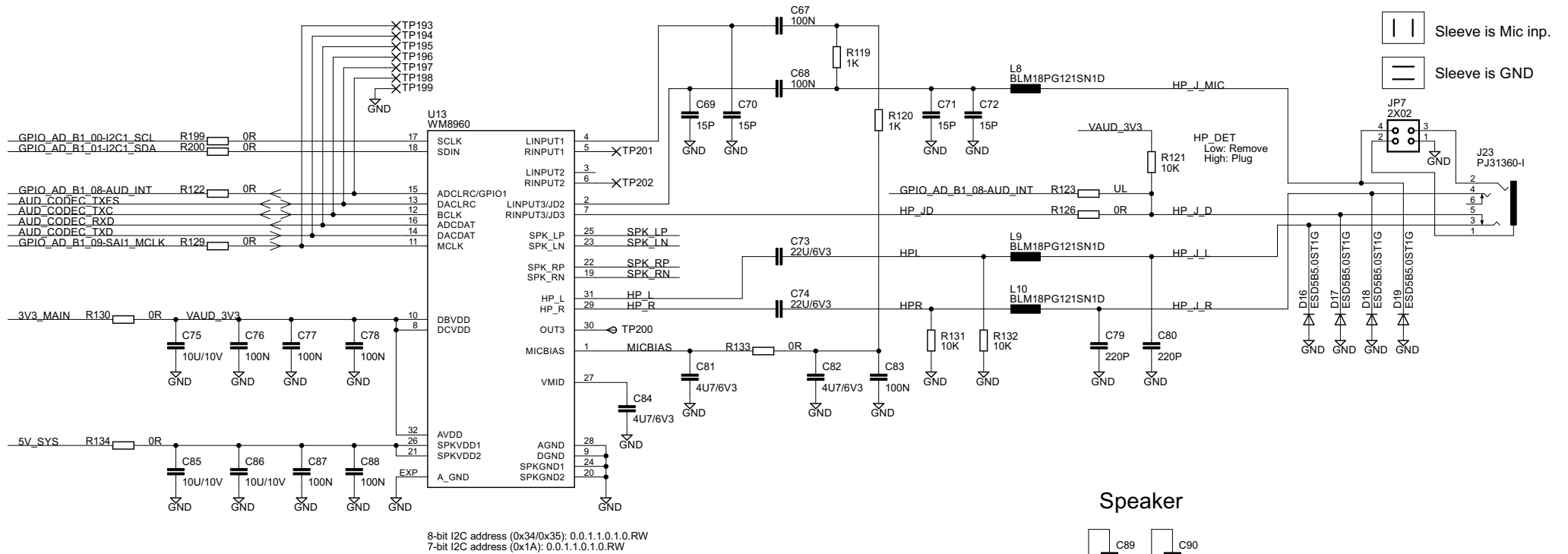
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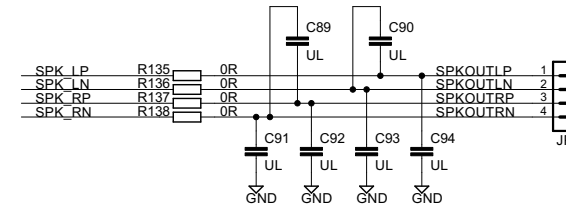
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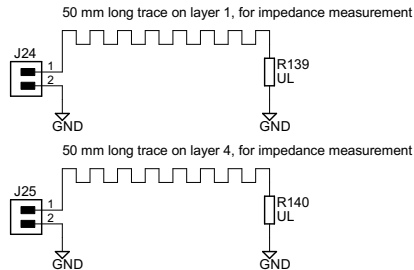
Audio Codec



Speaker



Impedance control



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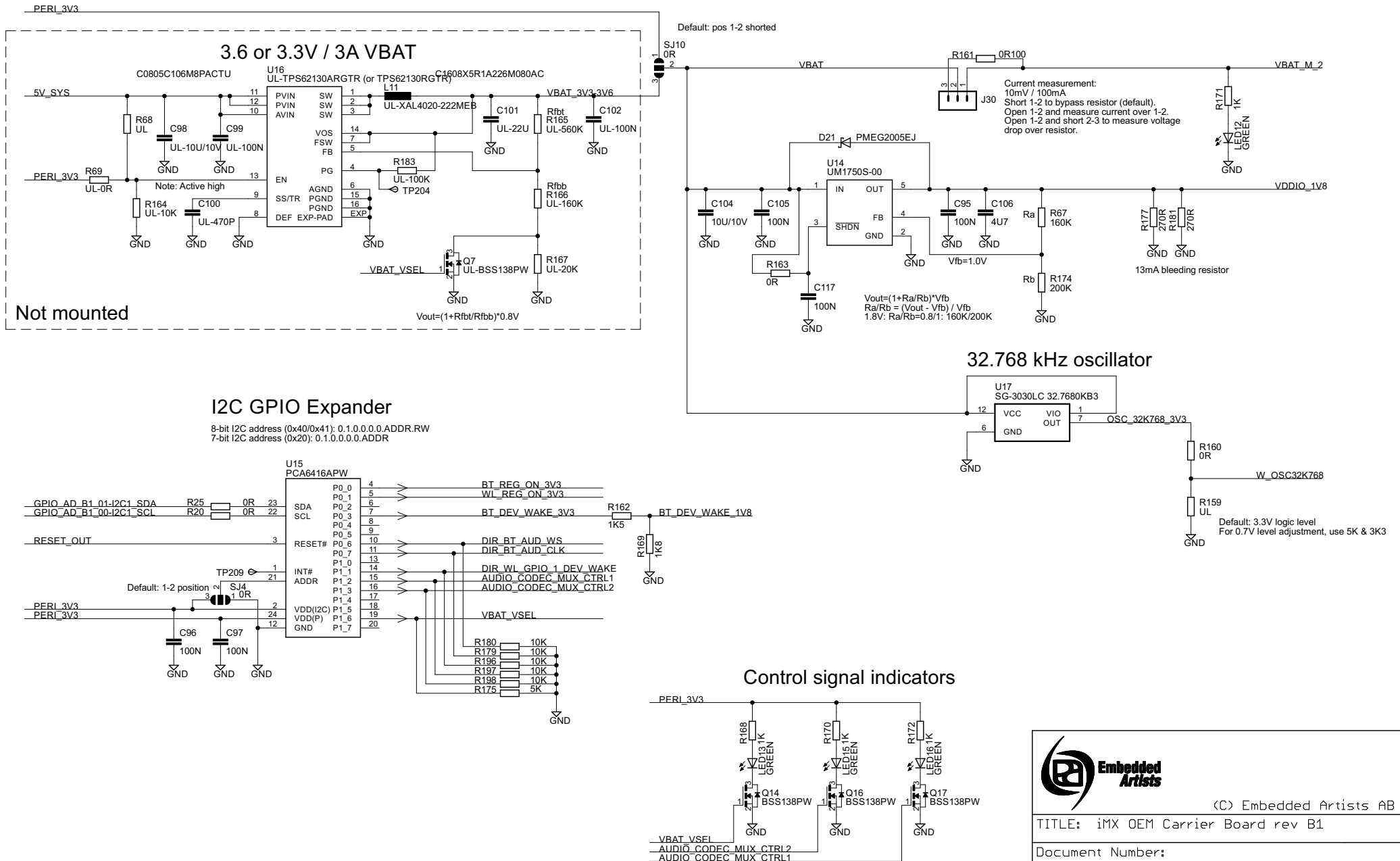
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M.2 Power Supply and Control



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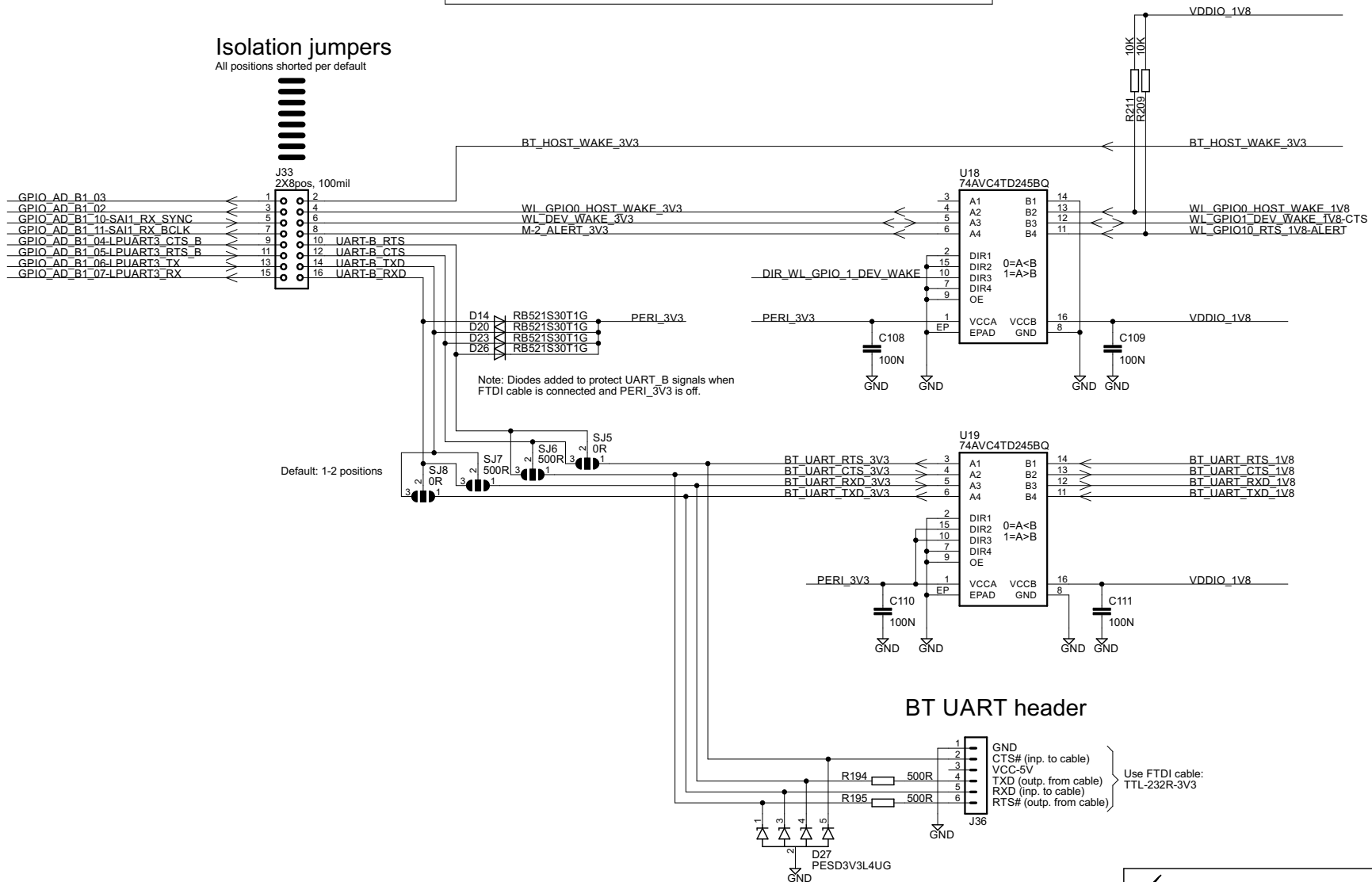
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Level Translation for BT UART and Control Signals



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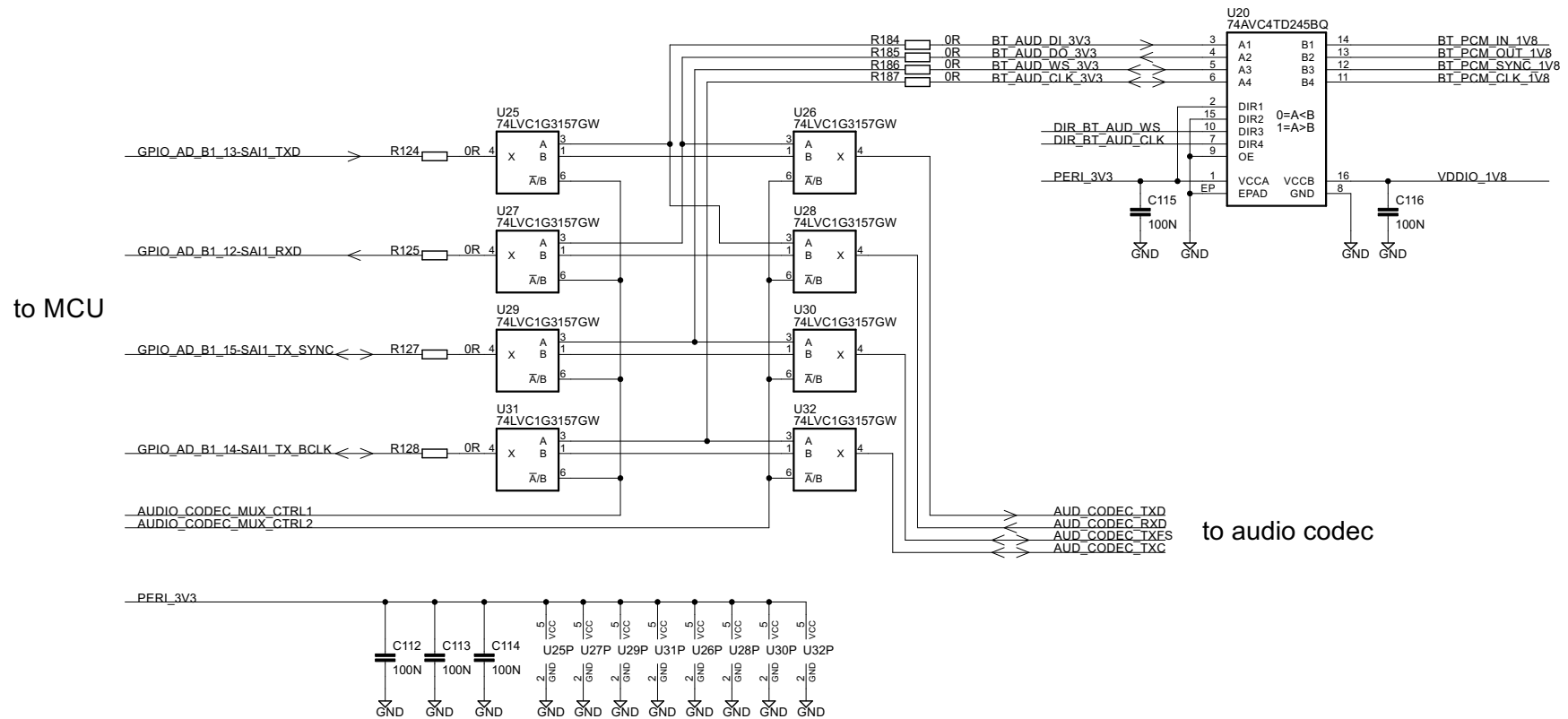
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Level Translation and Audio Signal Multiplexing



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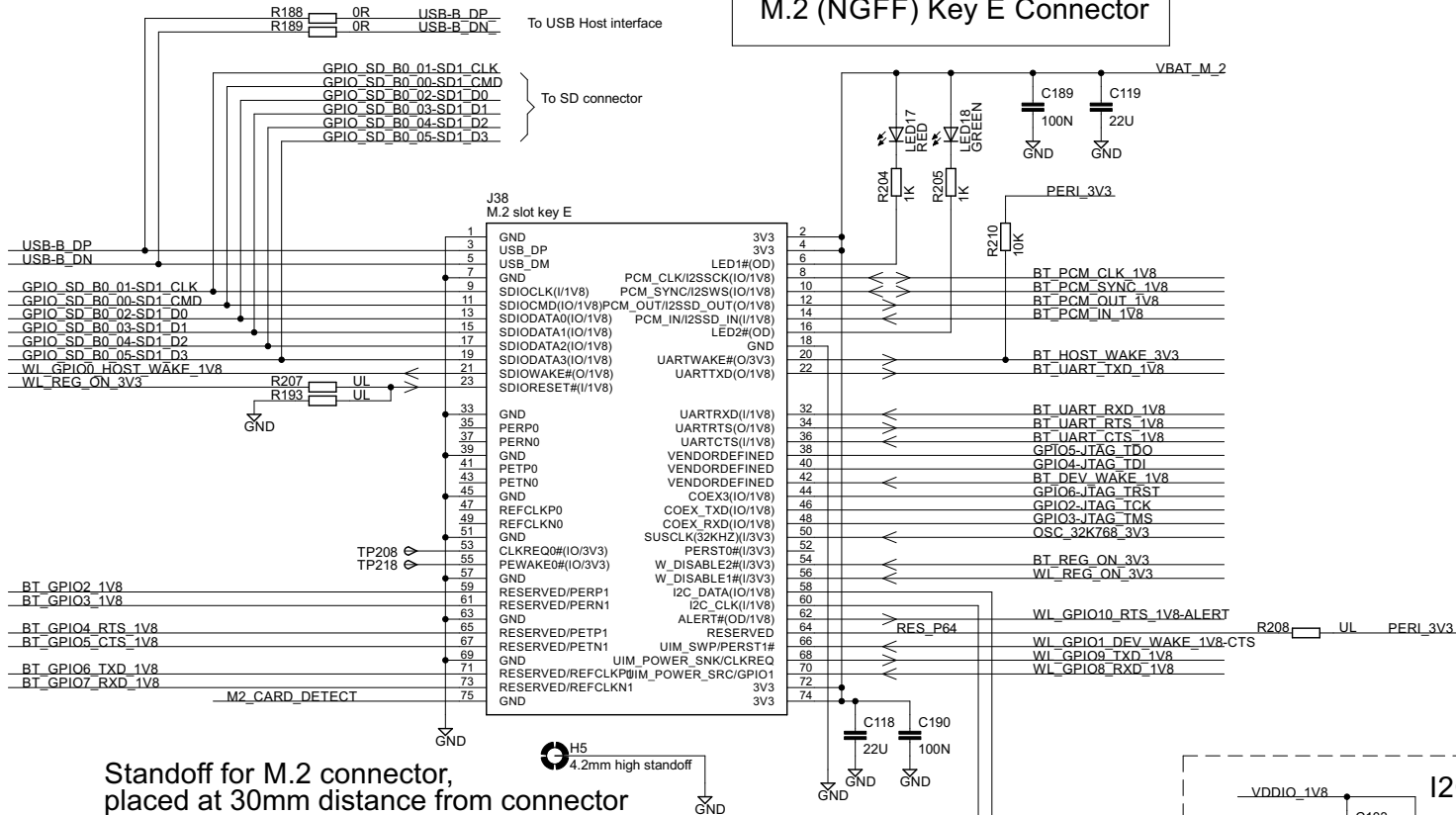
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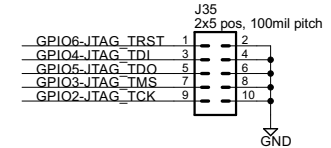
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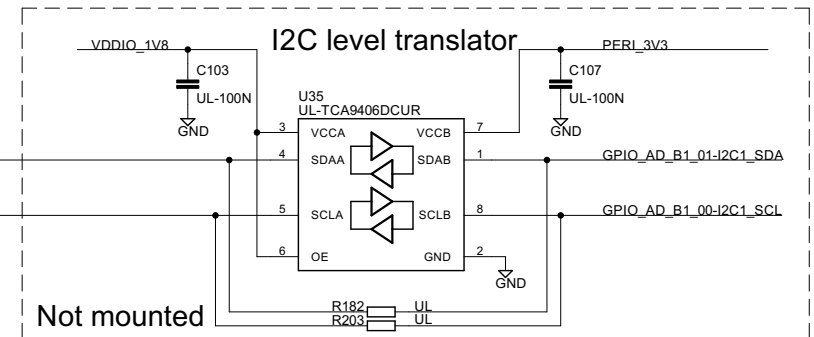
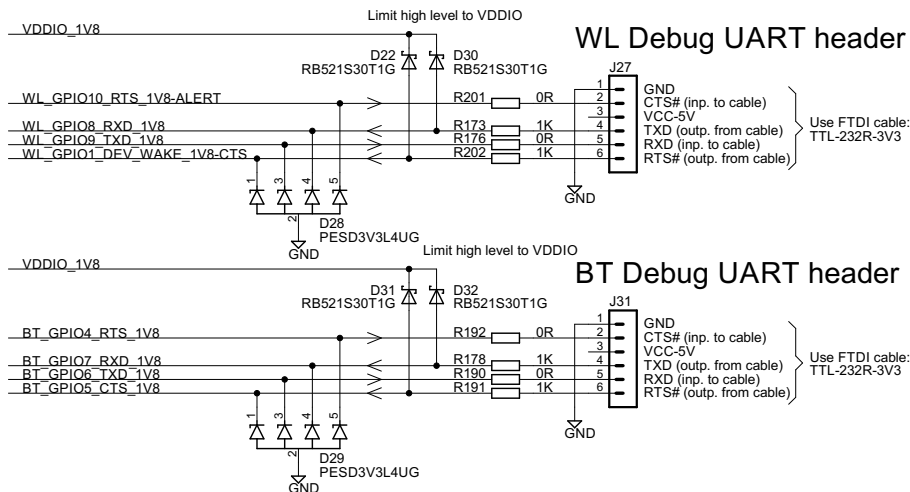
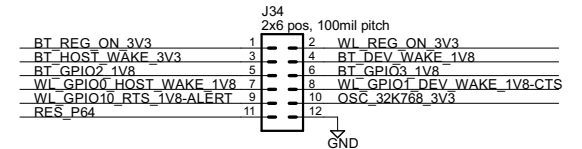
M.2 (NGFF) Key E Connector



WLAN JTAG



Access to signals



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